HW5 Report

3.2

3.3 There should be 3 NOPs between two R-TYPE instructions.

3.4 The limitation is that the register being tested is also used for calculation in the same time, and might be changed, and the BEQ won't notice the change.

3.5 a) While jump operations are executed, the next operations are being fetched & decoded. Can be prevented by NOPs.

b) The limitation in 3.4 can be solved by NOPs and Data Forwarding.

c) sw & lw can face MEM hazards, solved by forwarding.